



GP-2133  
41

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Serial No: 09/977,585 Docket No: 1981010  
Filing Date: 01/12/2001 Applicant: Chien-Tzu Hou  
Examiner: TRIMMINGS, JOHN P Art Unit: 2133  
Title: STRUCTURE AND METHOD OF REPAIRING SDRAM BY GENERATING  
SLICING TABLE OF FAULT DISTRIBUTION

To: Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

RESPONSE TO OFFICE ACTION

Dear Sir:

In a phone conference with the Examiner on 11/9/2004, Applicant has learned that the response filed June 17, 2004, was not received. Applicant's counsel hereby declares that the enclosed is a true and correct copy of the response originally filed June 17, 2004.

Thank you for your consideration.

Respectfully submitted,

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NOV 22 2004

Technology Center 2100

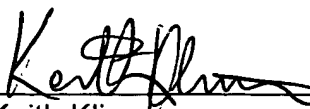
Date: November 11, 2004

  
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Certificate of Mailing (37 CFR 1.8a): I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Date: November 11, 2004

  
Keith Kline